Description

JMT N-channel Enhancement Mode Power MOSFET

Features

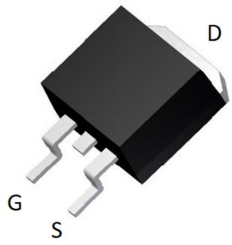
- 60V, 180A
  $R_{DS(ON)}$ < 3.8mΩ @ V_{GS} = 10V
- Advanced Trench Technology
- Provide Excellent $R_{DS(ON)}$ and Low Gate Charge
- Lead Free

Application

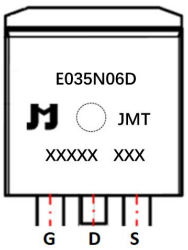
- Load Switch
- PWM Application
- Power Management

100% UIS TESTED!
100% ΔVds TESTED!

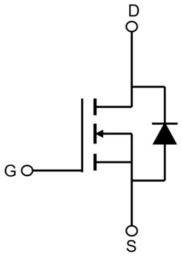
RoHS



TO-263-3L top view



Marking and pin Assignment



Schematic Diagram

Package Marking and Ordering Information

Device Marking	Device	Outline	Package	Reel Size	Reel (pcs)	Per Carton (pcs)
JMTE035N06D	JMTE035N06D	TAPING	TO-263-3L	13”	800	4000

Absolute Maximum Ratings (T_C=25℃ unless otherwise specified)

Symbol	Parameter		Max.	Units
V_{DSS}	Drain-Source Voltage		60	V
V_{GSS}	Gate-Source Voltage		±20	V
I_D	Continuous Drain Current	T_C = 25℃	180	A
		T_C = 100℃	117	A
I_{DM}	Pulsed Drain Current note1		720	A
EAS	Single Pulsed Avalanche Energy note2		324	mJ
P_D	Power Dissipation	T_C = 25℃	258	W
$R_{\theta JC}$	Thermal Resistance, Junction to Case		0.58	℃/W
T_J, T_{STG}	Operating and Storage Temperature Range		-55 to +175	℃

Electrical Characteristics (T_J=25℃ unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
Off Characteristics						
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	V_{GS}=0V, I_D=250μA	60	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	V_{DS}=60V, V_{GS}=0V,	-	-	1.0	μA
I_{GSS}	Gate to Body Leakage Current	V_{DS}=0V, V_{GS}= ±20V	-	-	±100	nA
On Characteristics						
$V_{GS(th)}$	Gate Threshold Voltage	V_{DS}=V_{GS}, I_D=250μA	2	3	4	V
$R_{DS(on)}$	Static Drain-Source on-Resistance note3	V_{GS}=10V, I_D=30A	-	2.9	3.8	mΩ
Dynamic Characteristics						
C_{iss}	Input Capacitance	V_{DS}=25V, V_{GS}=0V, f=1.0MHz	-	7660	-	pF
C_{oss}	Output Capacitance		-	642	-	pF
C_{rss}	Reverse Transfer Capacitance		-	620	-	pF
Q_g	Total Gate Charge	V_{DD}=30V, I_D=30A, V_{GS}=10V	-	138	-	nC
Q_{gs}	Gate-Source Charge		-	21	-	nC
Q_{gd}	Gate-Drain(“Miller”) Charge		-	33	-	nC
Switching Characteristics						
$t_{d(on)}$	Turn-on Delay Time	V_{DD}=30V, I_D=30A, R_L=1Ω, R_{GEN}=3Ω, V_{GS}=10V	-	14	-	ns
t_r	Turn-on Rise Time		-	10	-	ns
$t_{d(off)}$	Turn-off Delay Time		-	65	-	ns
t_f	Turn-off Fall Time		-	27	-	ns
Drain-Source Diode Characteristics and Maximum Ratings						
I_S	Maximum Continuous Drain to Source Diode Forward Current		-	-	180	A
I_{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	720	A
V_{SD}	Drain to Source Diode Forward Voltage	V_{GS}=0V, I_S=30A	-	-	1.2	V
t_{rr}	Body Diode Reverse Recovery Time	T_J=25℃, I_F=20A,dI/dt=100A/μs	-	52	-	ns
Q_{rr}	Body Diode Reverse Recovery Charge		-	75	-	nC

Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

2. EAS condition :Starting T_J=25℃,V_{DD}=40V,V_G=10V,L=0.5mH,Rg=25Ω,I_{AS}=36A

3. Pulse Test: Pulse Width≤300μs, Duty Cycle≤0.5%

Typical Performance Characteristics

Figure1: Output Characteristics

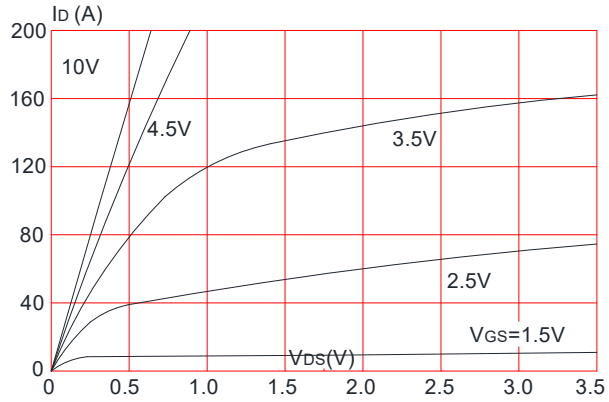


Figure 2: Typical Transfer Characteristics

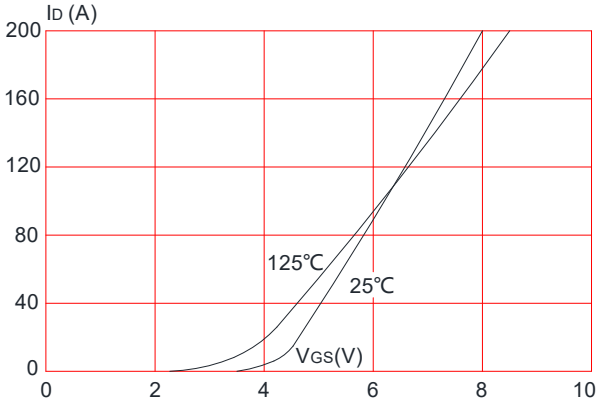


Figure 3: On-resistance vs. Drain Current

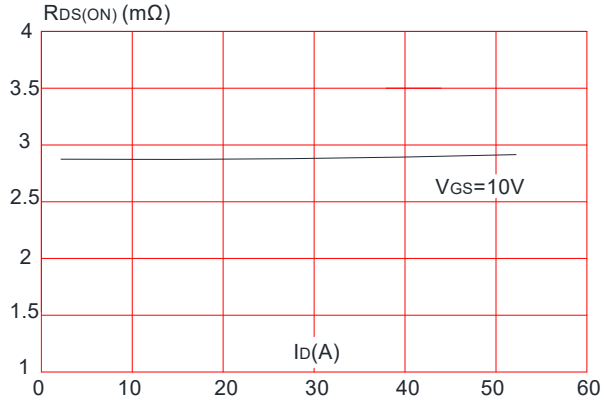


Figure 4: Body Diode Characteristics

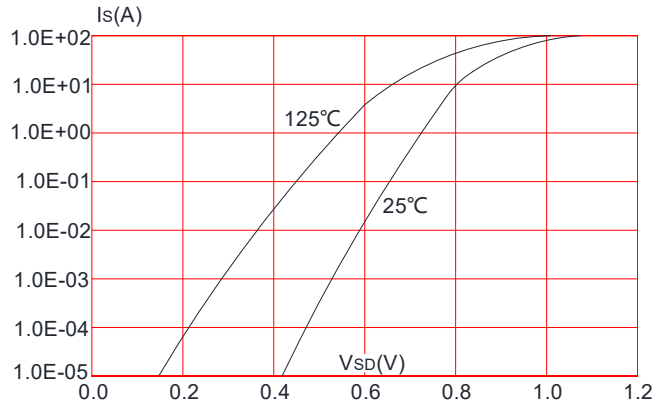


Figure 5: Gate Charge Characteristics

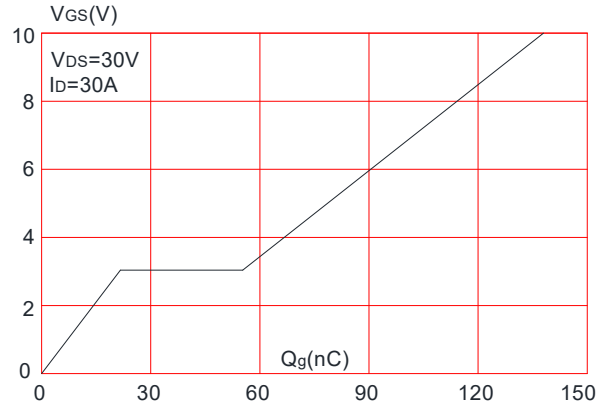


Figure 6: Capacitance Characteristics

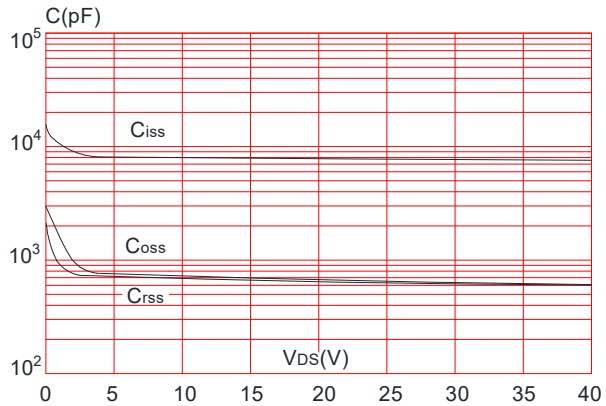


Figure 7: Normalized Breakdown Voltage vs. Junction Temperature

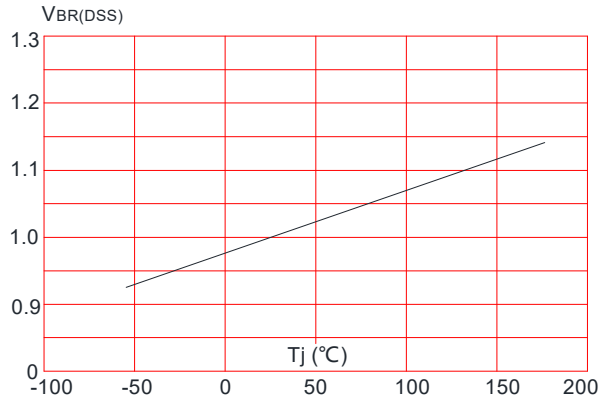


Figure 8: Normalized on Resistance vs. Junction Temperature

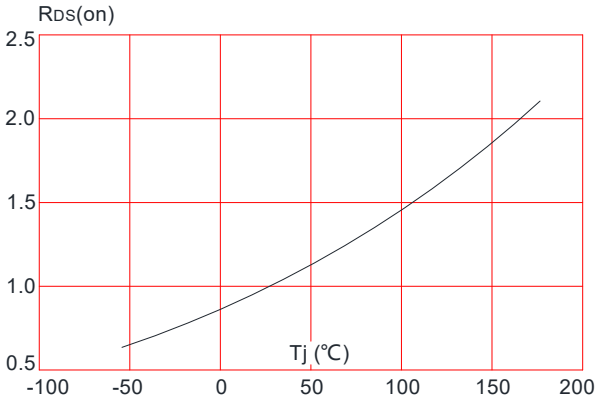


Figure 9: Maximum Safe Operating Area

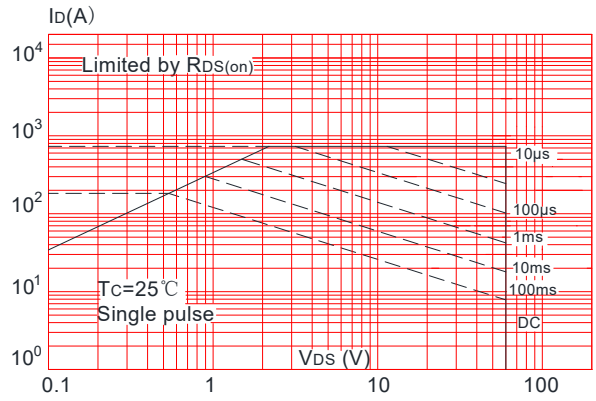


Figure 10: Maximum Continuous Drain Current vs. Case Temperature

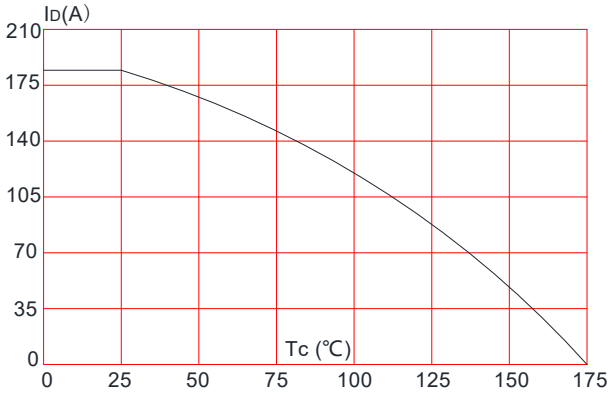
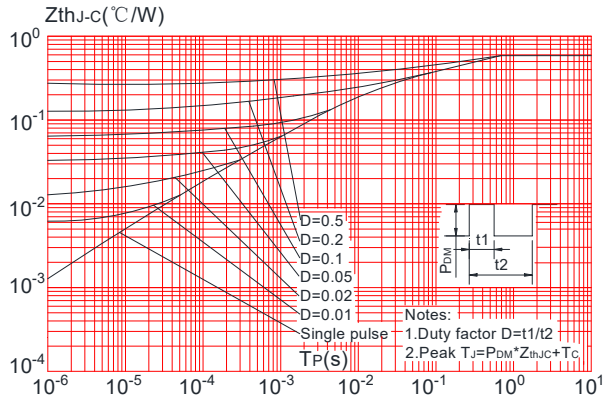


Figure.11: Maximum Effective Transient Thermal Impedance, Junction-to-Case



Test Circuit

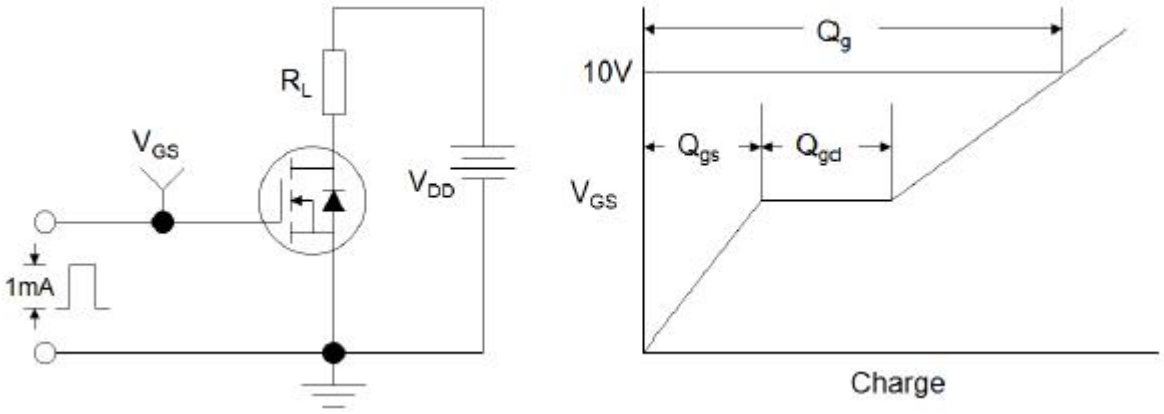


Figure1:Gate Charge Test Circuit & Waveform

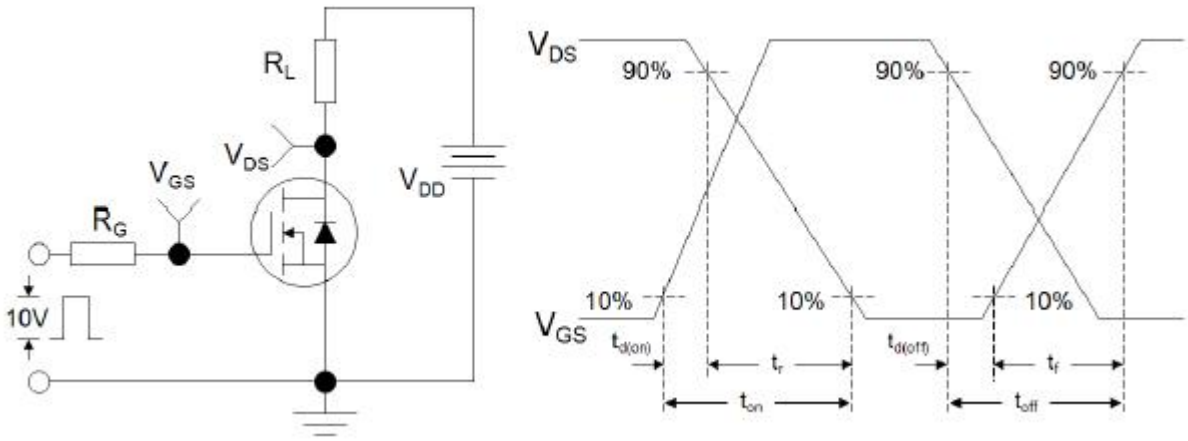


Figure 2: Resistive Switching Test Circuit & Waveforms

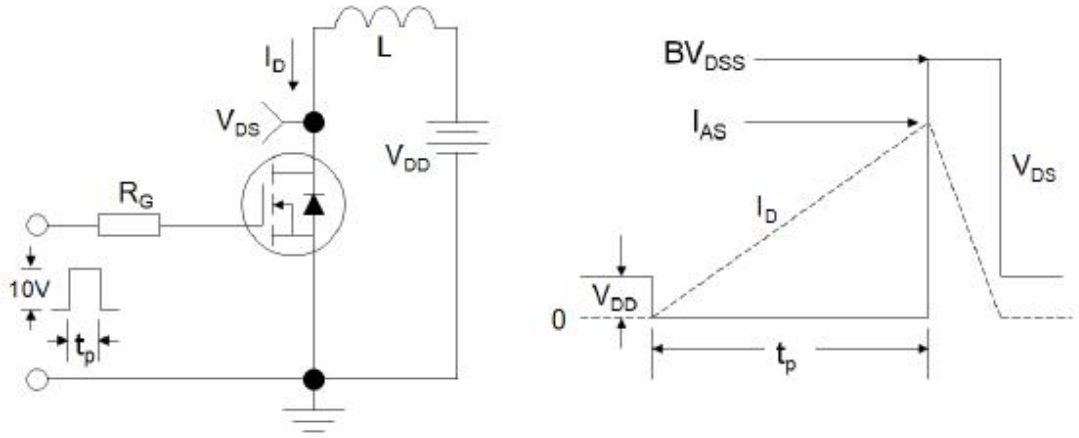


Figure 3:Unclamped Inductive Switching Test Circuit & Waveforms

Package Mechanical Data- TO-263-3L